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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application Of:  
WILLIAM W. WHITE, III. et al.

Application No. 10/086,474

Confirmation No. Unknown

Filed: March 1, 2002

For: SYTEM AND METHOD FOR OPTIMAL  
SETTING OF MESSAGE ACCEPTANCE  
FILTERS

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) Group Art Unit: NOT YET ASSIGNED  
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) Examiner: NOT YET ASSIGNED  
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Commissioner For Patents  
Washington, D.C. 20231

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Dear Sir:

Applicants submit herewith Form PTO-1449 and a copy of the references listed thereon.

Since this Information Disclosure Statement is being filed within three months of the filing date of the Application or before the mailing date of a First Office Action on the merits, whichever event occurs last (37 C.F.R. § 1.97(b)), no certification or fee is due. The Commissioner is hereby authorized to charge any deficiency in fees to Deposit Account No. 19-3875. A duplicate copy of this paper is enclosed for that purpose.

Respectfully submitted,

Dated: 6/17/02

By: Michael J. Femal  
Micheal J. Femal, Reg. No. 27,784  
Square D Company  
1415 South Roselle Road  
Executive Plaza  
Palatine, IL 60067-7399  
(847) 397-2600

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Karen Pyka  
Karen Pyka (148986)

Attorney Docket No. SAA-81 (402 P 278)



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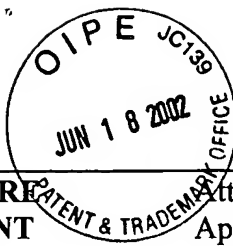
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Karen Pyka  
Karen Pyka (148986)



**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
PTO-1449

(148986)

Attorney Docket No. SAA-81 (402 P 278)

Application No.: NOT YET ASSIGNED

Applicant: William W. White, III.

Filing Date: March 1, 2002

Art Unit: NOT YET ASSIGNED

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**U.S. PATENT DOCUMENTS**

Examiner Initial	U.S. Patent Document No.	Name of Patentee or Applicant	Class/ Subclass	Date of Publication

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Foreign Patent Document			Name of Patentee or Applicant	Date of Publication	T
	Office	Number	Kind			

**OTHER PRIOR ART OR NON-PATENT LITERATURE DOCUMENTS**

Examiner Initial	(including Author (in capital letters), Title of the article, Title of the item, Date, Pages, Volume-Issue number, Publisher, City and/or Country where published.)	T
	"ST7 8-BIT MCU FAMILY USER MANUAL," STMicroelectronics, 315 pages, May 1999.	
	"ST7 FAMILY PROGRAMMING MANUAL," Rev. 1.1, 96 pages, March 1999.	
	"ST72521M/R/AR 8-BIT MCU WITH NESTED INTERRUPTS, FLASH, 10-BIT ADC, FIVE TIMERS, SPI, SCI, I <sup>2</sup> C, CAN INTERFACE Product Review," Rev. 1.3, STMicroelectronics, 195 pages, February 2001.	
	"ST72311R, ST72511R, ST72512R, ST72532R, 8-BIT MCU WITH NESTED INTERRUPTS, EEPROM, ADC, 16-BIT TIMERS, 8-BIT TIMERS, 8-BIT PWM ART, SPI, SCI, CAN INTERFACES Datasheet," Rev. 2.1, STMicroelectronics, 164 pages, February 2000.	
	"ST72311R, ST72511R, ST72532R, 8-BIT MCU WITH NESTED INTERRUPTS, EEPROM, ADC, 16-BIT TIMERS, 8-BIT PWM ART, SPI, SCI, CAN INTERFACES," Rev. 2.6, STMicroelectronics, 164 pages, November 2000.	

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